

**METHOD AND APPARATUS FOR PERIODIC PHASE ALIGNMENT****ABSTRACT**

In one form, apparatus for aligning clock signals includes first and second logic circuitry  
5 for receiving respective first and second clock signals. The first and second clock signals are  
substantially synchronized and operations of the first logic circuitry and second logic circuitry  
are clocked by the respective first and second clock signals. The first logic circuitry receives a  
third clock signal derived from the second clock signal, and by repeatedly sampling the third  
clock signal with the first clock signal, the first logic circuitry repeatedly detects relative phase  
0 relations of the first and third clock signals. The second logic circuitry adjusts the phase of the  
third clock signal responsive to an accumulation of the phase relation detecting.

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